

*JFW*  
503.30370CC8



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: R. SAITO, et al.

Application No.: 10/807,272

Filed: March 24, 2004

For: SEMICONDUCTOR MEMORY DEVICE AND METHOD OF  
OPERATION THEREOF

Art Unit: 2818

Examiner: Trong Q. Phan

**SUPPLEMENTAL AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

December 17, 2004

Sir:

In supplement to the Amendment filed on December 9, 2004, please further  
amend the above-identified application as listed below and set forth on the following  
pages:

**Amendments to the Claims**

**Remark follow the amendment.**

01/10/2005 STANLEY 061-00000000

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